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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Number	10727973	Docket Number	CNTR.2071		
Filed	12/4/2003	Group Art Unit	2135		
Examiner	LEYNNA HA	Customer No.	23669		
Application Title	APPARATUS AND METHOD FOR PERFORMING TRANSPARENT BLOCK CIPHER CRYPTOGRAPHIC FUNCTIONS				
First Named Inventor	G. GLENN HENRY				

	First Named I	Inventor	G. GLENN HENRY
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			FACSIMILE COVER SHEET
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F	ax Number:	(661) 4	60-1986
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First Named Inventor	G. GLENN HENRY				

## INFORMATION DISCLOSURE STATEMENT TRANSMITTAL

VIA FACSIMILE: 571-273-8300

Mail Stop AMENDMENT Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Attached hereto is Form PTO-1449 submitted under 37 CFR 1.97(b) listing documents believed relevant to the subject application. It is respectfully requested that the examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims, and return an initialed copy of each form to the undersigned practitioner.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 CFR 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR 1.56(b) or is available as a reference under 35 U.S.C. §102 et seq. Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 CFR 1.56, 1.97, and 1.98, and the Manual of Patent Examining Procedures §609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

Some of the enclosed documents may have markings thereon. No significance is intended to be attached to the markings.

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Respectfully submitted, / Richard K. Hullman/ RICHARD K. HUFFMAN, P.E. Registration No. 41,082 Tel: (719) 575-9998 03/18/2001 Date: \_

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Substitute for form 1449A/PTO		Application Number	10727973		
INFORM	IATION DIS	CLOS	JRE	Filing Date	12/4/2003
STATEMENT BY APPLICANT				First Named Inventor	G. Glenn Henry
					2135
(use as many sheets as necessary				Examiner Name	Leynna Ha
Sheet	1	of	2	Attorney Docket Number	CNTR.2071

			U.S. PATENT	DOCUMENTS	
Examiner Cite	Cite	Document Number	Publication Date	Name of Patentee or	Pages, Columns, Lines, Where
Initials*	No.1	Number-Kind Code <sup>2(# known)</sup>	MM-DD-YYYY	Applicant of Cited Document	Relevant Passages or Relevant Figures Appear
/L.H./		4168396	09-18-1979	Robert M. Best	
/L.H./		4278837	07-14-1981	Robert M. Best	
/L.H./		4465901	08-14-1984	Robert M. Best	
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FOREIGN PATENT DOCUMENTS								
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Initials*	No.1	Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5 (f known)</sup>		Applicant of Cited Document	Where Relevant Passages or Relevant Figures Appear			
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Examiner	/Laynna Ha/ (03/00/2000)	Date 03/09/2009	
Signature	/Leynna Ha/ (03/09/2009)	Considered 00/00/2000	j

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Substitute for form 1449A/PTO				Application Number	10727973	
INFORM	ATION DISC	LOS	URE	Filing Date	12/4/2003	
STATEMENT BY APPLICANT				First Named Inventor	G. Glenn Henry	
				Group Art Unit	2135	
(use as	s many sheets as ne	ecessary		Examiner Name	Leynna Ha 🕠	
Sheet	2	of	2	Attorney Docket Number	CNTR.2071	

•		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/L.H./	AA	Dimond et al. "Application-specific Customisation of Multi-Threaded Soft Processors." Computers and Digital Techniques. IEEE Proceedings. Volume 153, Issue 3. 2 May 2006. Pages 173-180.	
/L.H./	AB	Anderson et al. "Cryptographic Processors - A Survey." Proceedings of the IEEE. Volume 94, Issue 2. Feb. 2006. Pages 357-369.	
/L.H./	AC	Eberle et al. "Architectural Extensions for Elliptic Curve Cryptography over GF(2") on 8-bit Microprocessors." Application- Specific Systems. Architecture Processors. 2005. ASAP 2005. 16th IEEE International Conference on 23-25 July 2005. Pages 343-349.	
L.H./	AD	KUHN, Markus G. "Cipher Instruction Search Attack on the Bus-Encryption Security Microcontroller DS5002FP." Computers, IEEE Transactions on Volume 47, Issue 10. Oct. 1998. Pages 1153-1157.	
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Examiner	/Leynna Ha/ (03/09/2009)	Date	03/09/2009
Signature	/Leyinia ria/ (US/US/2009)	Considered	00/00/2000

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